



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Varadarajan Srinivasan et al

SERIAL NO.: 10/613,776

FILING DATE: 7/2/2003

Title: *Rollover Bits for Packet Departure Time Calculator*

Examiner: unknown

Art Unit: 2661

Confirmation No: 6968

SUBMISSION OF FORMAL DRAWINGS

Attention: Official Draftsman

Assistant Commissioner for Patents
Alexandria, VA 22313

Sir:

Applicant submits 16 sheets of formal drawings, consisting of Figures 1, 2, 3, 4, 5A, 5B, 6, 7, 8, 9, 10, 11, 12A, 12B, 13, 14, 15, 16, 17, and 18. If there are any questions regarding these drawings, please call the undersigned at (415) 291-9497.

Respectfully submitted,

Dated: March 16, 2004

William L Paradice III
Reg. No. 38,990

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313 on March 16, 2004.

By:

William L Paradice III